



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

MOMTAZ et al.

Application No.: Not Assigned

Filed: Herewith

For: HIGH FREQUENCY LOSS OF  
SIGNAL DETECTOR

Examiner: Unassigned

Art Unit: Unassigned

INFORMATION DISCLOSURE  
STATEMENT UNDER 37 CFR §1.97 and  
§1.98

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that no fee is required for submission of this statement, since it is being submitted prior to the first Office Action. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Babak S. Sani', with a long horizontal flourish extending to the right.

Babak S. Sani  
Reg. No. 37,495

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Please type a plus sign (+) inside this box



PTO/SB/08A (08-00)

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Substitute for form 1449A/PTO

# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet **1** of **2**

## **Complete if Known**

Application Number  
Filing Date  
First Named Inventor **MOMTAZ, AFSHIN**  
Group Art Unit  
Examiner Name  
Attorney Docket Number **019717-003100US**

10879 U.S. PTO  
10/09/02



## **U.S. PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
	AA	6,115,154		Antoniades et al.	09/05/00	
	AB	US 6,222,668	B1	Dutrisac et al.	04/24/01	
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
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	AJ					
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	AL					
	AM					
	AN					
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	AQ					
	AR					
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	AT					

Examiner  
Signature

Date  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

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Sheet **2** of **2**

## **Complete if Known**

Application Number	
Filing Date	
First Named Inventor	MOMTAZ, AFSHIN
Group Art Unit	
Examiner Name	
Attorney Docket Number	019717-003100US

Jc 979 U.S. PTO  
 10/09/2166  
 03/04/02

## **OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	AU	ALEXANDER GILMAN et al., "Monolithic Clock and Data Recovery Chip for 10Gb/s Fiber Communication Systems," <u>Final Report 1999-2000 for MICRO Project 99-078</u> , Industrial Sponsor: Conexant Systems, Department of Electrical and Computer Engineering, University of California, Santa Barbara, California USA	
	AV	RICHARD C. WALKER, et al., "A 2.488 GB/s Si-Bipolar Clock and Data Recovery IC with Robust Loss of Signal Detection," <u>HP Design Conference, Tokyo</u> , 1997, Hewlett Packard Laboratories, Palo Alto, California USA	
	AW		
	AX		
	AY		
	AZ		
	AAA		
	ABB		
	ACC		
	ADD		
	AEE		

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